

PI5C34X245

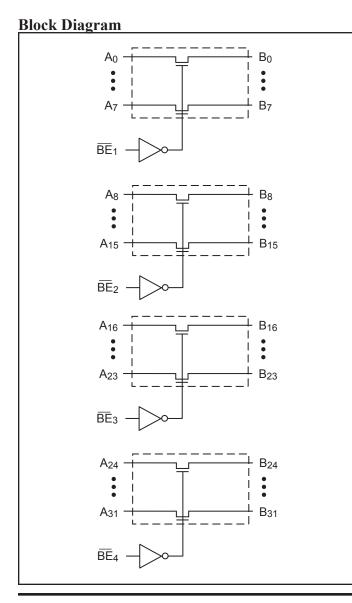
32-Bit, 2-Port Bus Switch

Features

- Near-Zero propagation delay
- 5Ω switches connect inputs to outputs
- · Direct bus connection when switches are ON
- Ultra-low quiescent power (0.2µA typical)
 Ideally suited for notebook applications
- Industrial operating temperature: -40°C to +85°C
- Packaging (Pb-free & Green available):
 80-pin BQSOP (B)

Description

The PI5C34X245 is a 32-bit, 2-port bus switch. Four enable signals (\overline{BEn}) turn the switches on. The bus switch creates no additional propagational delay or additional ground bounce noise.



Pin Configuration

iguration					
Iguration NC A0 A1 A2 A3 A4 A5 A6 A7 GND NC A8 A9 A10 A11 A12 A13 A14 A15 GND NC A14 A15 GND NC A16 A17 A18 A19 A20 A21 A23 GND NC A24 A25 A26 A27 A28		$1 \bigcirc 2$ 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 27	$\begin{array}{cccc} 79 \\ 78 \\ 77 \\ 76 \\ 75 \\ 74 \\ 73 \\ 72 \\ 71 \\ 70 \\ 69 \\ 66 \\ 64 \\ 63 \\ 66 \\ 64 \\ 63 \\ 62 \\ 61 \\ 60 \\ 59 \\ 58 \\ 57 \\ 56 \\ 55 \\ 54 \\ 53 \\ 52 \\ 51 \\ 50 \\ 48 \\ 47 \\ 46 \\ 45 \\ \end{array}$	$\begin{array}{c} V_{CC} \\ \overline{BE}_1 \\ B_0 \\ B_1 \\ B_2 \\ B_3 \\ B_4 \\ B_5 \\ B_6 \\ B_7 \\ V_{CC} \\ \overline{BE}_2 \\ B_8 \\ B_9 \\ B_{10} \\ B_{11} \\ B_{12} \\ B_{13} \\ B_{14} \\ B_{15} \\ V_{CC} \\ \overline{BE}_3 \\ B_{16} \\ B_{17} \\ B_{18} \\ B_{19} \\ B_{20} \\ B_{21} \\ B_{22} \\ B_{23} \\ V_{CC} \\ \overline{BE}_4 \\ B_{25} \\ B_{26} \\ B_{27} \\ \end{array}$	
	- 1			B ₂₅	
A ₂₇		35	46		
A ₂₈	q	36	45		
A ₂₉	q	37	44	B ₂₈	
A ₃₀	þ	38	43	B ₂₉	
A ₃₁	þ	39	42	B ₃₀	
GND	þ	40	41	B ₃₁	
				-	

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Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	-65° C to $+150^{\circ}$ C
Ambient Temperature with Power Applied	-40°C to +85°C
Supply Voltage to Ground Potential	-0.5V to +7.0V
DC Input Voltage	-0.5V to +7.0V
DC Output Current	
Power Dissipation	0.5W

Stresses greater than those listed under MAXIMUM RAT-INGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Truth Table⁽¹⁾

Function	B En	A0-A31
Disconnect	Н	Hi-Z
Connect	L	B0-31

Note:

1. H = High Voltage Level

L = Low Voltage Level

Hi-Z = High Impedance

Pin Description

Note:

Pin Name	I/O	Description
$\overline{\text{BE}}_{\text{X}}$	Ι	Bus Enable Input (Active LOW)
A ₀ -A ₃₁	I/O	Bus A
B ₀ -B ₃₁	I/O	Bus B

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}C$ to $+85^{\circ}C$, $V_{CC} = 5V \pm 10\%$)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			v
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	
I _{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1	
IIL	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μΑ
I _{OZH}	High Impedance Output Current	0 - A, B - V _{CC}			±1	
V _{IK}	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18mA$		-0.7	-1.2	V
IOS	Short Circuit Current ⁽³⁾	$A(B) = 0V, B(A) = V_{CC}$	100			mA
V_{H}	Input Hystersis at Control Pins					V
D	Switch On-Resistance ⁽⁴⁾	$V_{CC} = Min., V_{IN} = 0V, I_{ON} = 48mA$		5	7	Ω
R _{ON}	Switch On-Resistance	$V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA$		10	15	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

- 2. Typical values are at V_{CC} = 5.0V, T_A = 25°C ambient and maximum loading.
- 3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- 4. Measured by the voltage drop between A and B pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (A,B) pins.

Capacitance ($T_A = 25^{\circ}C$, f = 1 MHz)

Paramaters ⁽¹⁾	Description	Test Conditions	Max.	Units
C _{IN}	Input Capacitance		6	
C _{OFF}	A/B Capacitance, Switch OFF	$V_{IN} = 0V$	6	pF
C _{ON}	A/B Capacitance, Switch ON		12	

Notes:

1. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditio	ons ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max.	$V_{IN} = GND$ or V_{CC}		0.1	3.0	μΑ
ΔI _{CC}	Supply Current per Input @ TTL HIGH	V _{CC} = Max.	$V_{IN} = 3.4 V^{(3)}$			2.5	mA
I _{CCD}	Supply Current per Input per MHz ⁽⁴⁾	V _{CC} = Max., A & B Pins Open Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.

2. Typical values are at $V_{CC} = 5.0V$, +25°C ambient.

3. Per TTL driven input (V_{IN} = 3.4V, control inputs only); A and B pins do not contribute to I_{CC}.

4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics over Operating Range

			PI5C	34X245	
Parameter	Description	Conditions	C	om.	Units
			Min.	Max.	
t _{PLH} t _{PHL}	Propagation Delay ^(1,2) Ax to Bx, Bx to Ax			0.25	
t _{PZH} t _{PZL}	Bus Enable Time \overline{BEx} to Ax or Bx	$\begin{array}{l} C_L = 50 p F \\ R_L = 500 \Omega \end{array}$	1.5	5.6	ns
t _{PHZ} t _{PLZ}	Bus Disable Time $\overline{BE}x$ to Ax or Bx		1.5	5.2	

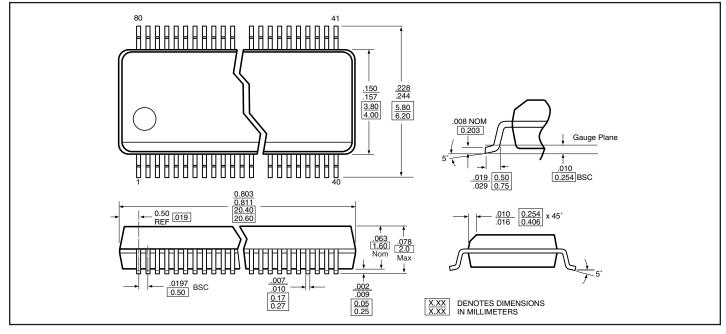
Notes:

1. This parameter is guaranteed but not tested on Propagation Delays.

2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.



Packaging Mechanical: 80-Pin, 150 Mil Wide Plastic BQSOP (B)



Ordering Information

Ordering Code	Packaging Code	Package Type
PI5C34X245B	В	80-Pin 150 Mil Wide Plastic BQSOP
PI5C34X245BE	В	Pb-free & Green, 80-Pin 150 Mil Wide Plastic BQSOP

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel